

FIG. 1

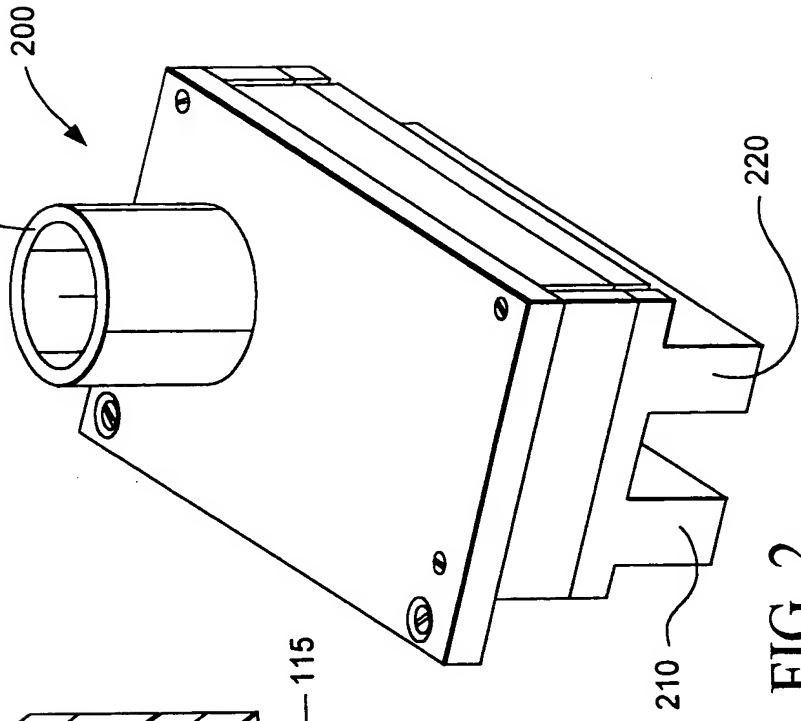


FIG. 2

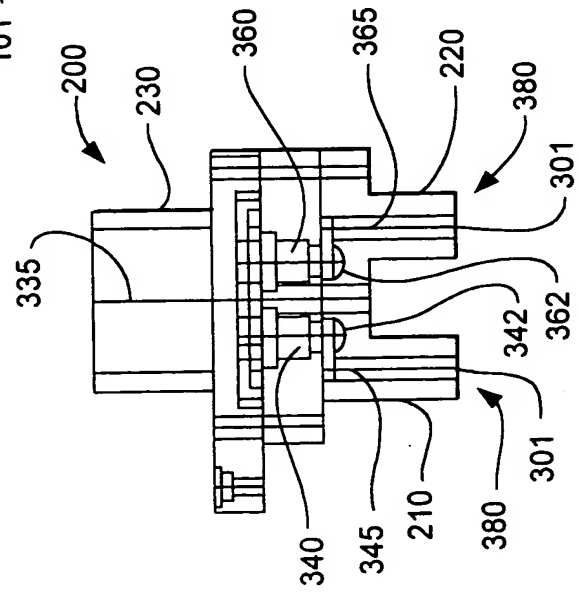


FIG. 3

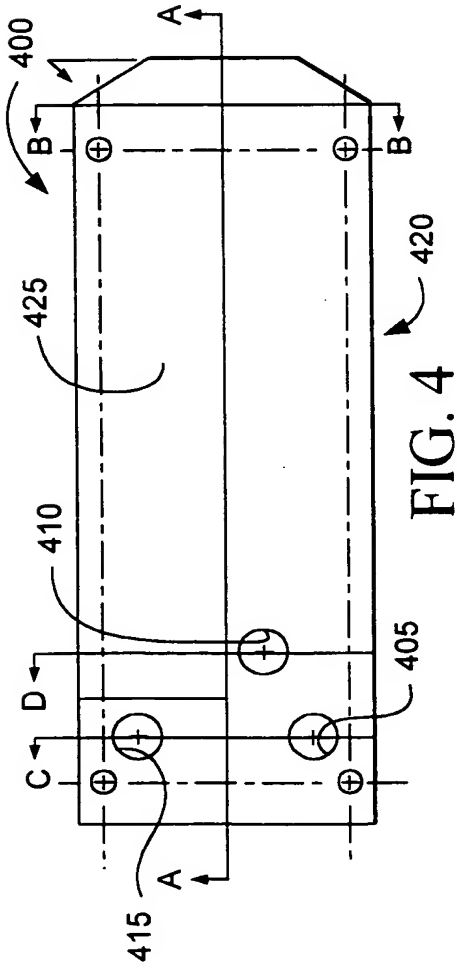


FIG. 5

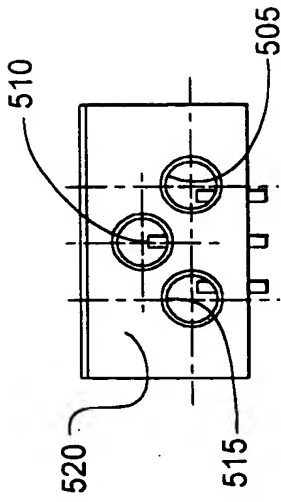


FIG. 6

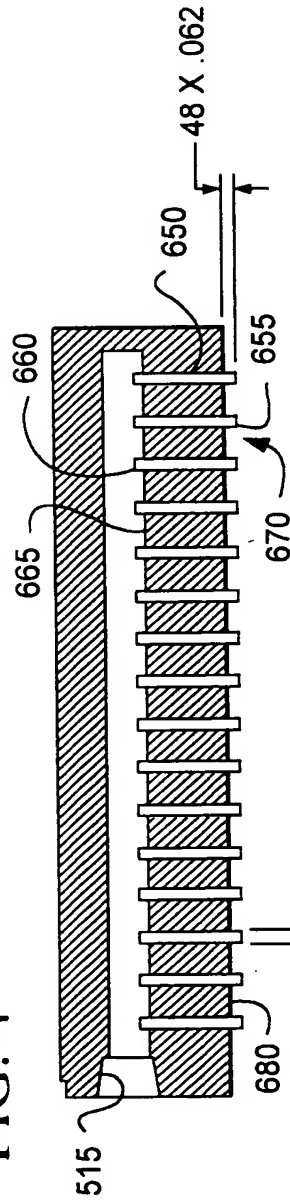


FIG. 7

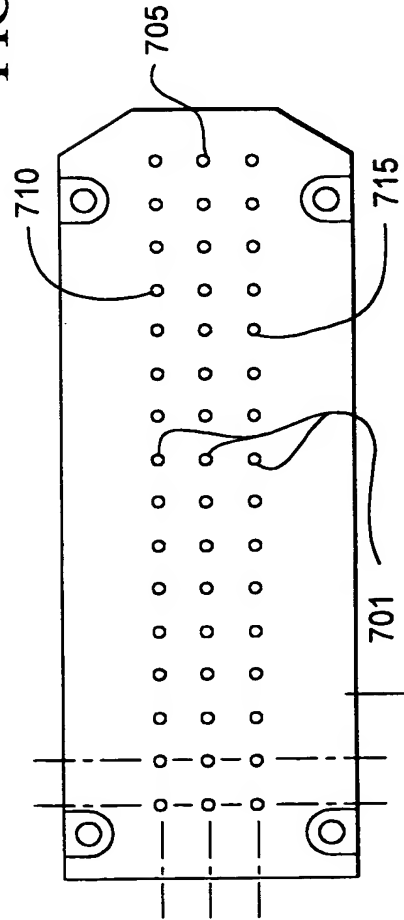
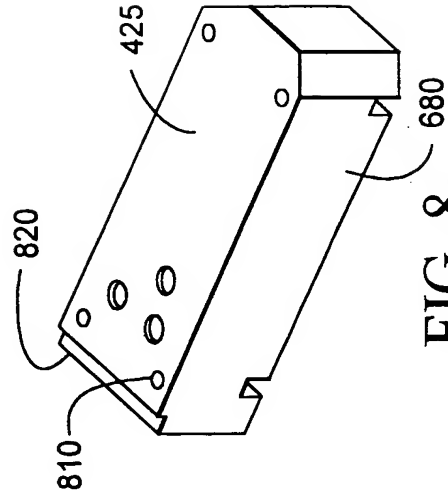


FIG. 8



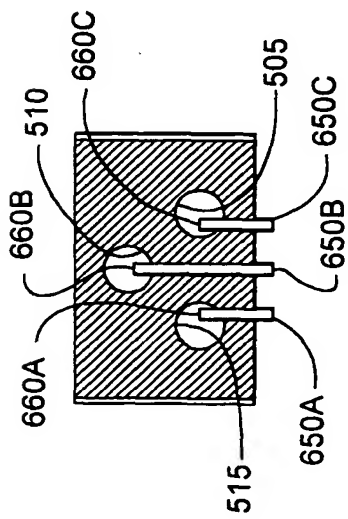


FIG. 9

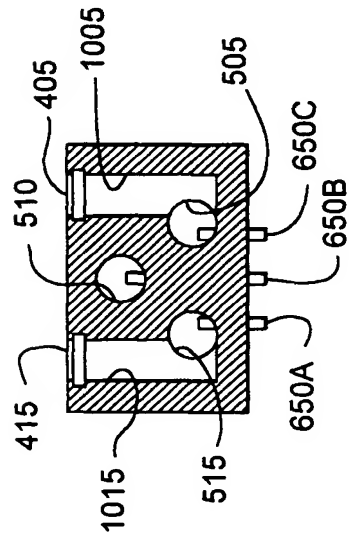


FIG. 10

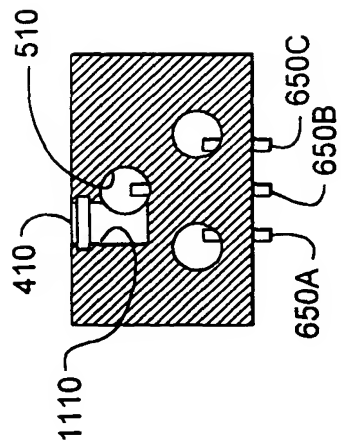


FIG. 11



FIG. 12A

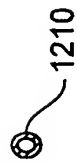


FIG. 13A

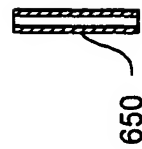


FIG. 12B

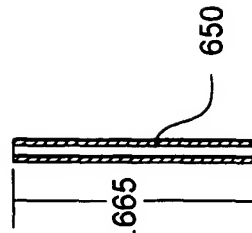


FIG. 13B

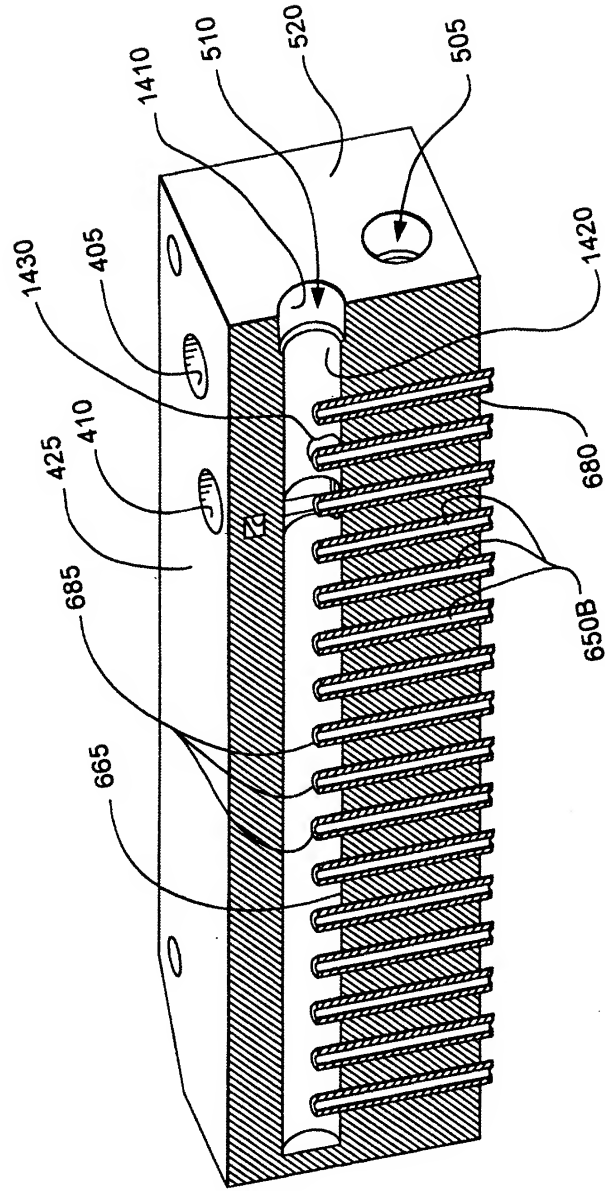


FIG. 14

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=0mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

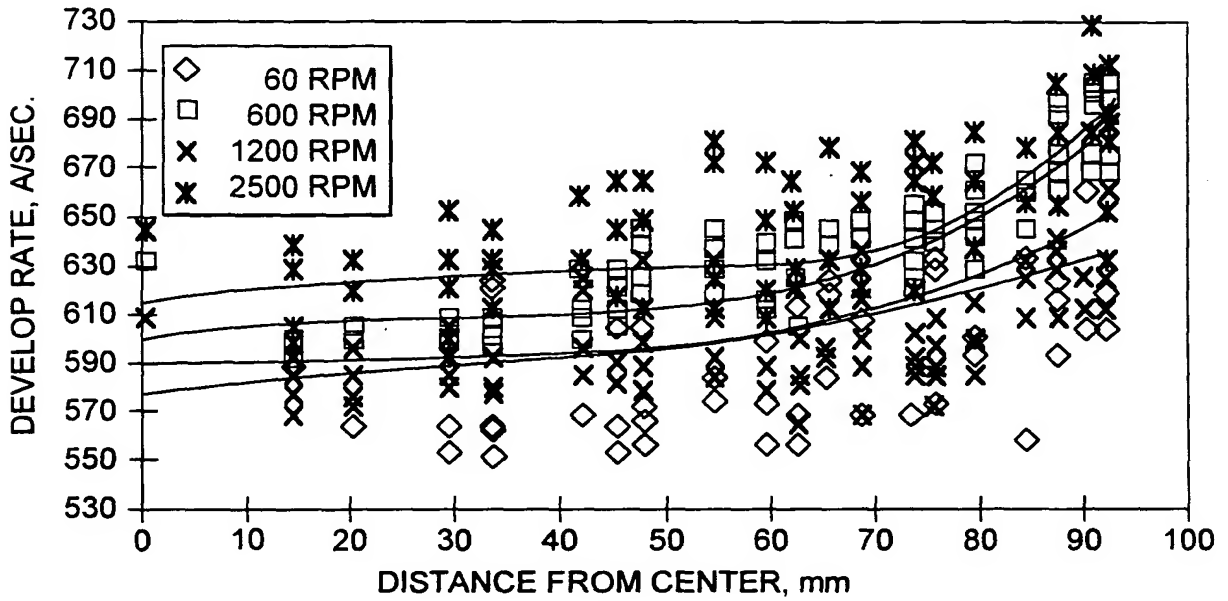


FIG. 15

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=5mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

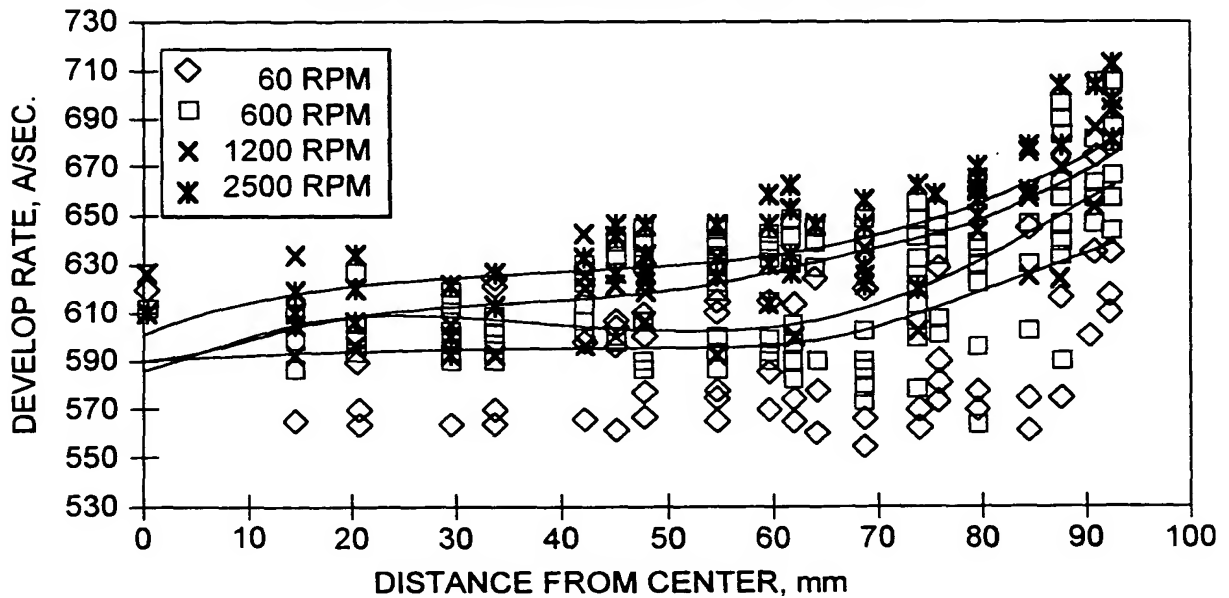


FIG. 16

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=10mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

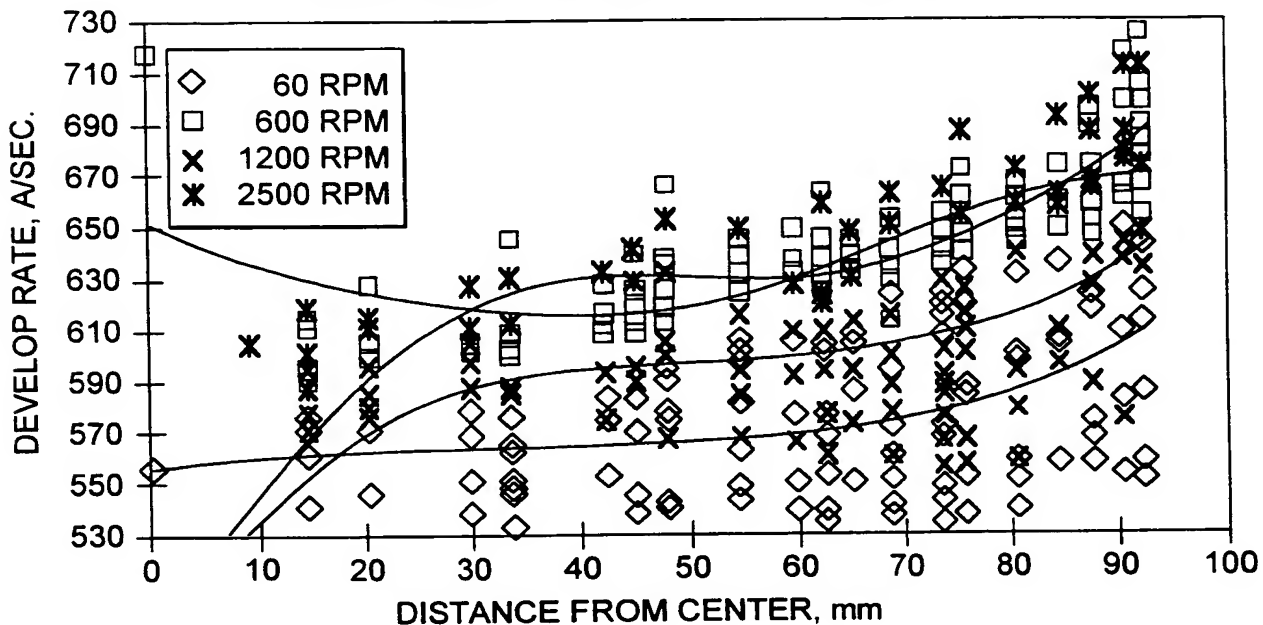


FIG. 17

DEVELOP RATES AT 121 WAFER LOCATIONS, DEVELOP ARM OFFSET=20mm
TOK 9; DOSE=12; DEVELOP TIME=10SEC.

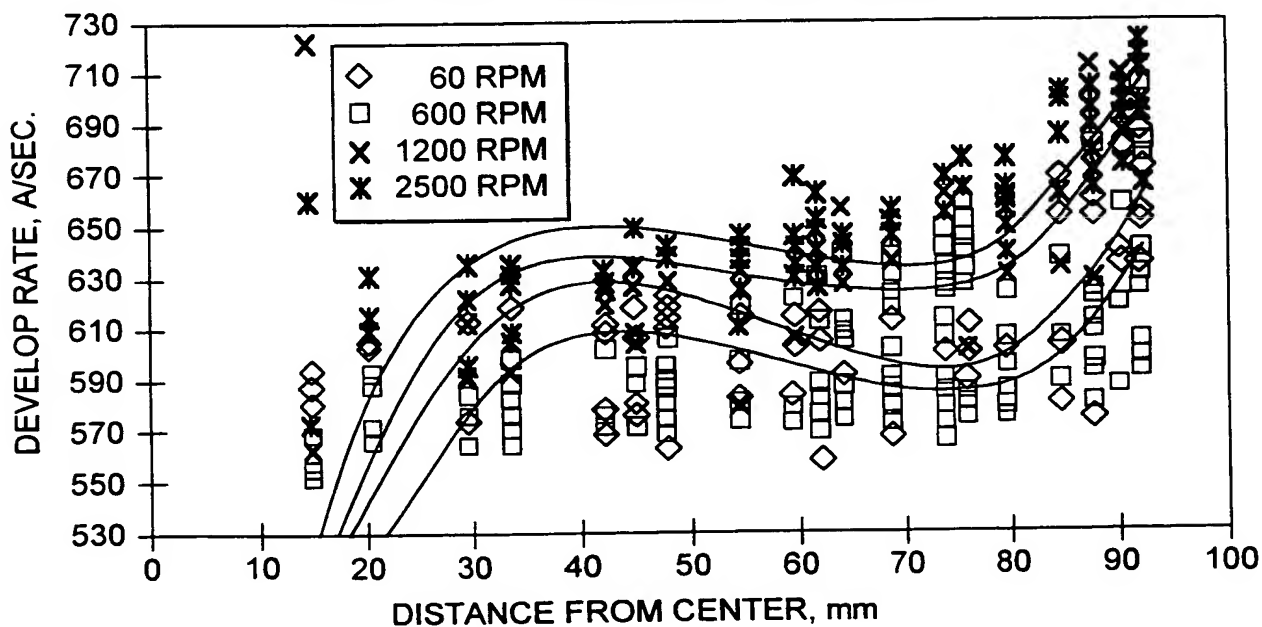


FIG. 18

FIG.19A

FIG.19B

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=0mm

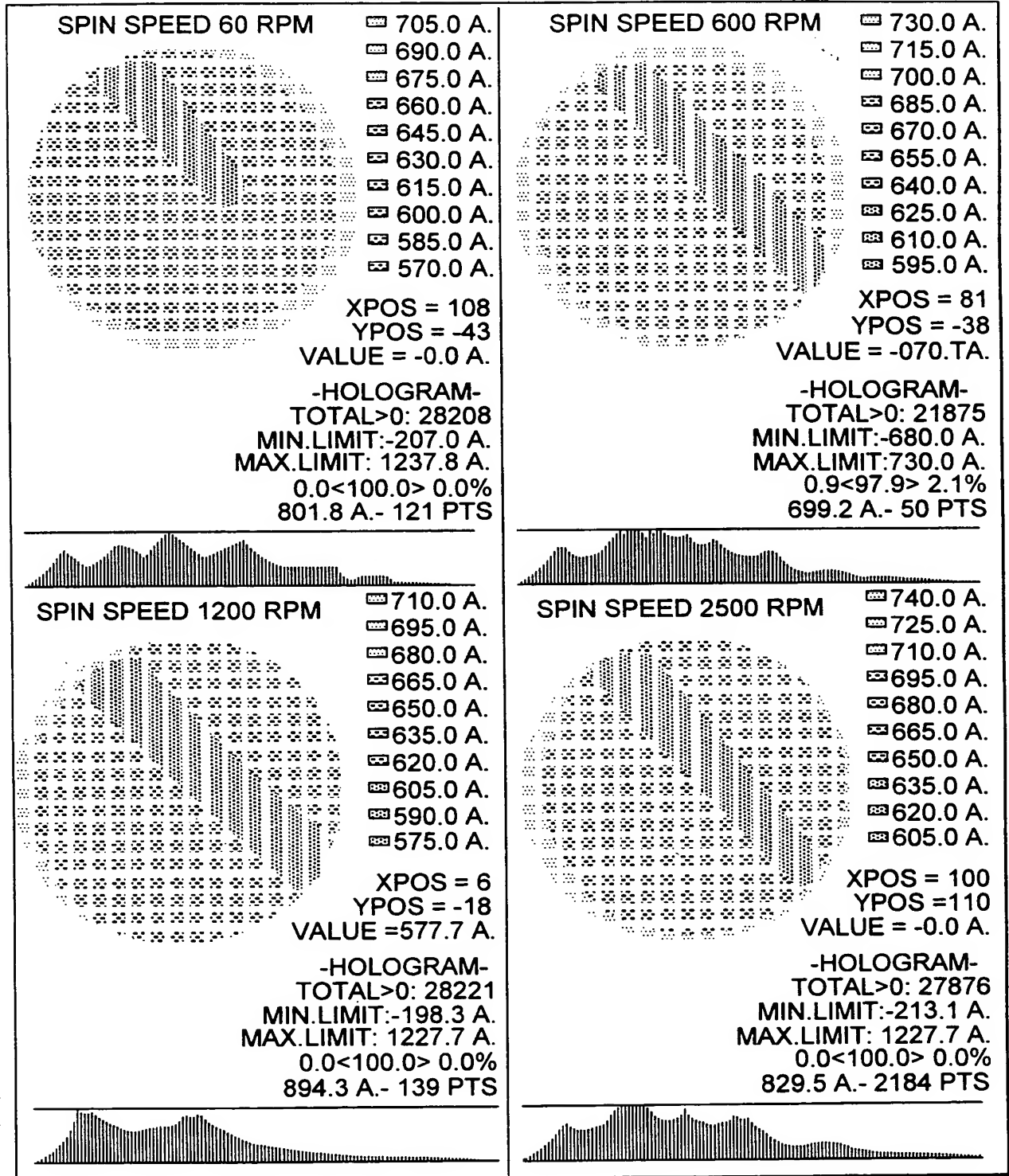


FIG.19C

FIG.19D

FIG.20A

FIG.20B

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=5mm

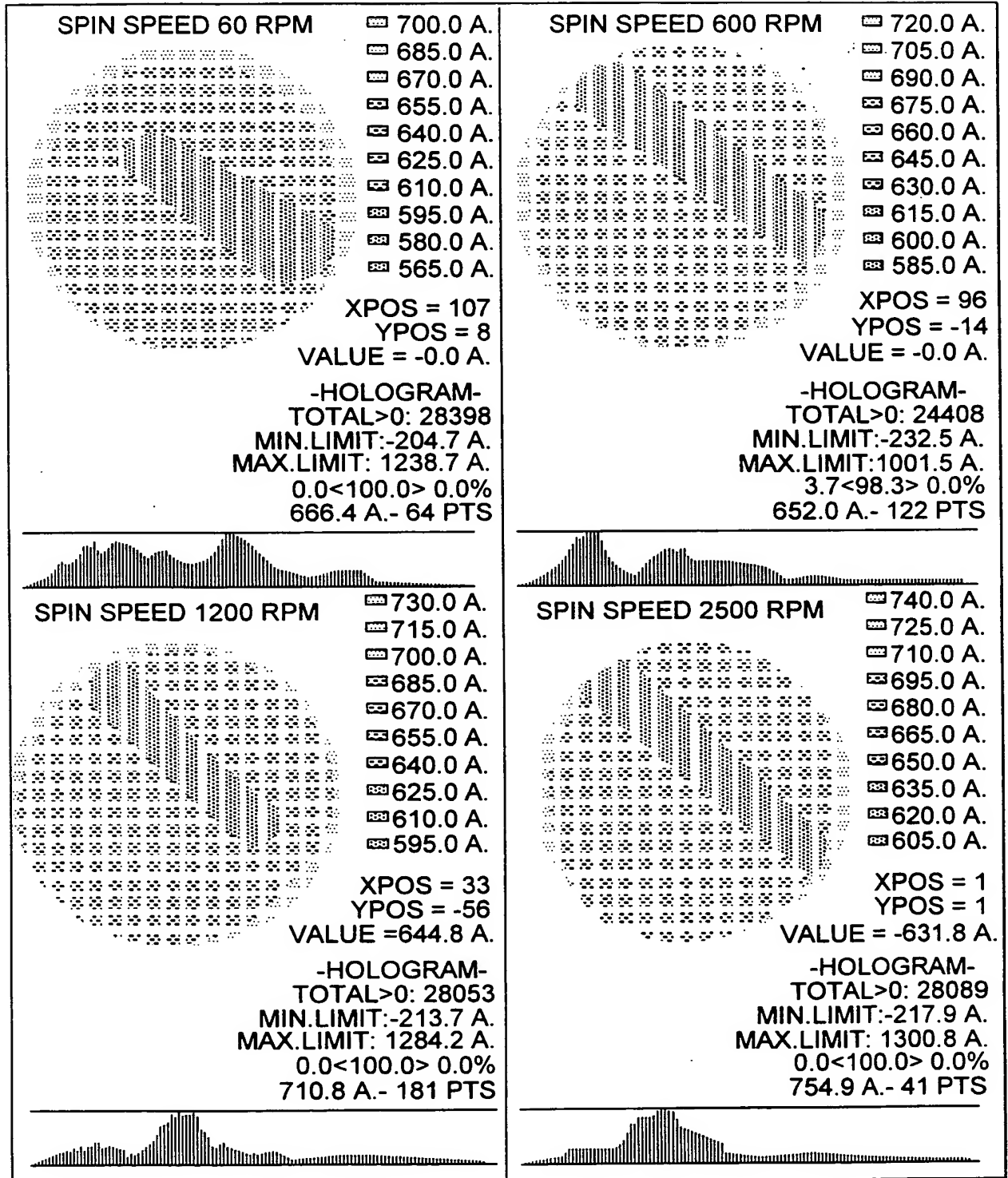


FIG.20C

FIG.20D

FIG.21A

FIG.21B

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=10mm

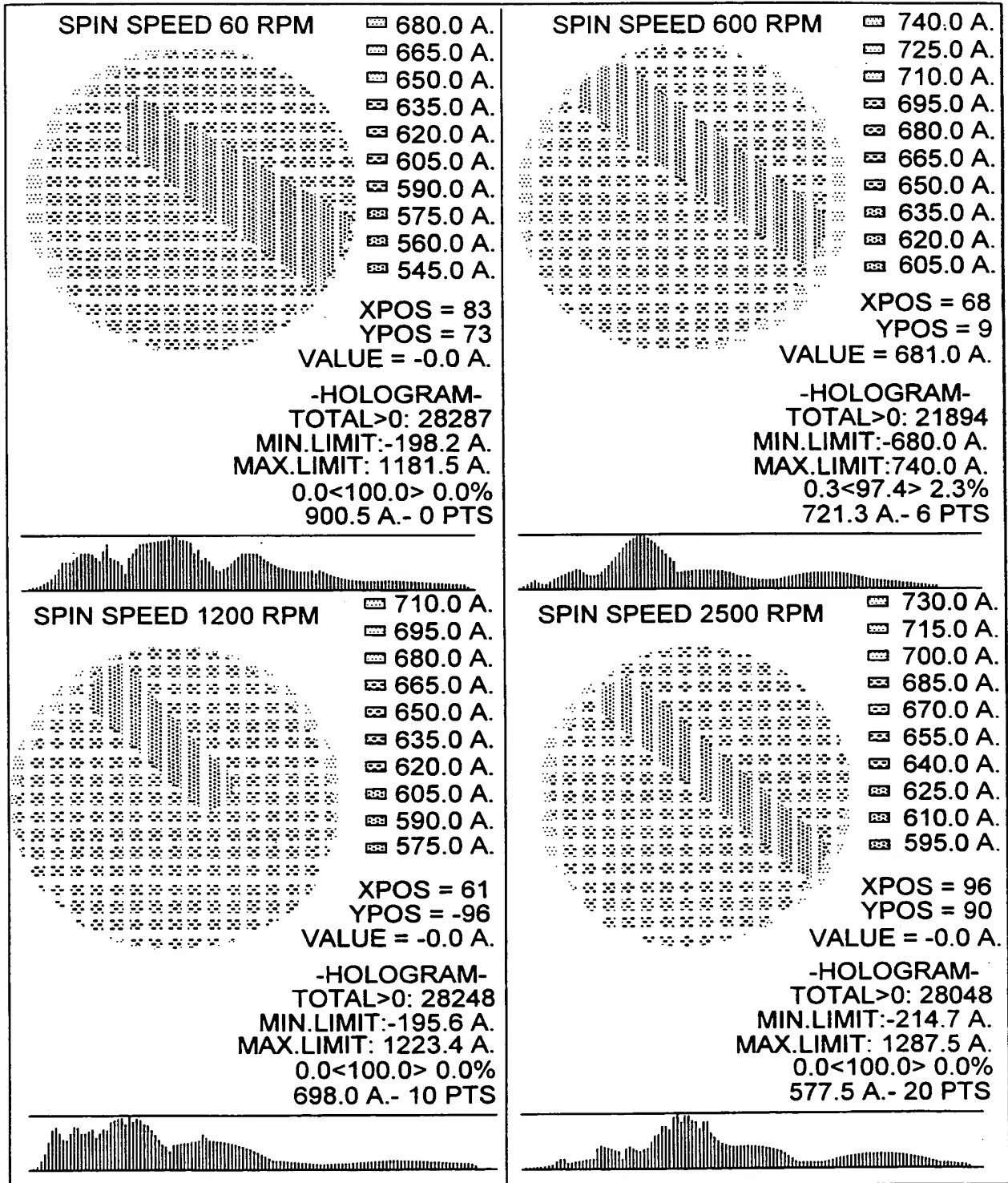


FIG.21C

FIG.21D

FIG.22A

FIG.22B

IPEC DEVELOP RATE MAPS FOR ARM OFFSET=20mm

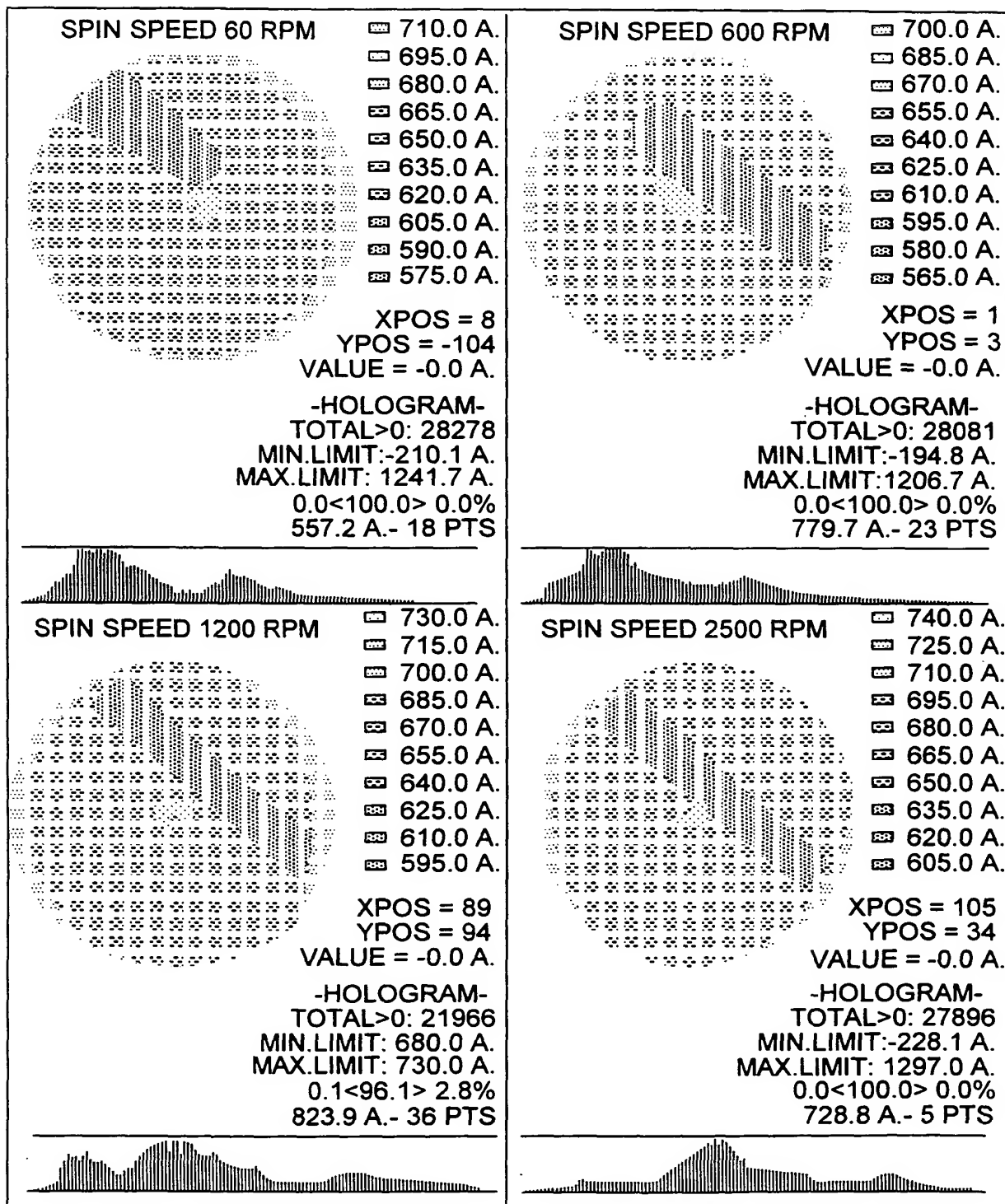


FIG.22C

FIG.22D